

ATTACHMENT 2

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BALL GRID ARRAY PACKAGE WITH HEAT SINK DEVICE

BACKGROUND OF THE INVENTION

1. Field of the Invention

¶0001 The present invention generally relates to a heat sink device applied for to an integrated circuit, and more particularly to a package device with a heat sink device to reduce the thermal resistance and to improve the efficiency of thermal dissipation.

2. Description of the Prior Art

¶0002 In the electronics and computer industries, it has been well known to employ various types of electronic package devices and integrated circuit chips, such as the PENTIUM central processing unit chip (CPU) manufactured by Intel Corporation. These integrated circuit chips have a pin grid array (PGA) package and are typically installed into a socket. These integrated circuit chips are installed onto a computer circuit board by soldering. These integrated circuit device for example the CPU microprocessor chips, generate a great deal of heat during operation which must be removed to prevent adverse effects. The PENTIUM microprocessor contain millions of transistors, and they are highly susceptible to overheating. Therefore, the microprocessor device itself or other components proximal to the microprocessor are easy to be destroyed because of overheating.

¶0003 In addition to the above discussion, there are many other types of semiconductor package devices, which are commonly used in computer equipment. At least some of these

semiconductor package devices also have the above-mentioned problem. For example, the resistors and thermistors generate large volumes of heat during normal operation, and if the heat can not be dissipated quickly to cool down the resistors and thermistors they can be destroyed and damaged by the heat.

¶0004 Also, the solid-state devices are commonly being installed onto a circuit board, or be installed into a motherboard or other similar primary circuit board in turn. For example, microprocessors, such as the PENTIUM II and the Celeron from Intel, are so-called “processor cards” which are installed into a motherboard of a computer in similar fashion to the way a modem is installed into a motherboard. The existing processor card, as known, has the processor semiconductor devices, such cache chips, or the like, and they are necessary for the operation of the card. The processor package may be installed into the processor card via a pin grid, ball grid, and land grid array and with a socket such as a ZIF or ball grid socket.

¶0005 Similarly, according to the earlier semiconductor devices discussed above, there are many different types of electronic devices which suffer from overheating. For example, any electronic package device may have a threat of overheating, and need to be cooled down to prevent overheating. However, the devices are too small to adequately support and receive the typical metallic heat sink. These prior metallic heat sinks are commonly glued directly to the electronic device with a thermally conductive adhesive, or installed into the electronic package device with a mechanical structure, such as a spring clip. Further, gap pads out the interface surface between the package device and the dissipating structure are often required to achieve thermal dissipating efficiency. In view of the foregoing issues related to these types of electronic components, providing heat dissipation in the form of heat sinks, and the like, is difficult and cost prohibitive.

¶0006 The foregoing heat sink assembly of the prior art suffers from the disadvantages of having multiple components and the high cost associated therewith. These multiple component heat sink assemblies typically include expensive machined or extruded heat conductive metal, such as

aluminum. Other parts, such as springs or addition clips require separate machining steps and/or molds for production. Therefore, these assemblies and methods are completely inappropriate for most electronic devices.

¶0007 FIG. 1 and FIG. 2 illustrate a conventional ball grid array package 100 with heat slug. The ball grid array package device with heat slug includes a ball grid array substrate 102, a chip or die 104 that is located on the ball grid array substrate 102, and a modified heat slug 106 that is positioned over the chip 104 and the ball grid array substrate 102. Then, a molding compound 108 is injected into the ball grid array package device 100 to accomplish the ball grid array packaging process. Referring to FIG. 2, the die or chip 104 is covered by the molding compound 108, and the heat dissipating path is restricted by the low thermal conductivity of the molding compound 108. So the ball grid array package device 100 can not get good heat dissipating efficiency. The solution of this problem is to add an embedded heat slug 108 onto the die or chip 104 to increase the heat dissipating area. Nevertheless, the defect of this technique is that if the die or chip 104 generate a large volume of heat, the heat cannot be removed to the environment sufficiently to reduce the operating temperature of the die or chip 104. Therefore, the chip or die 104 cannot be operated.

SUMMARY OF THE INVENTION

¶0008 It is an object of this invention is to provide a package device with a heat sink assembly thereon, wherein the heat sink assembly is made by casting to increase the heat dissipating efficiency.

¶0009 It is another object of this invention is to provide a thermal block on the backside of the first heat sink assembly to associate the cavity of the ball grid array package device with modified embedded heat slug to improve the heat dissipating efficiency.

¶0010 It is a further object of this invention is to provide at least two thermal supports on the backsides of first heat sink assembly to join the openings of the ~~bottom~~ plate second heat sink assembly.

¶0011 It is yet another object of this invention is to provide a second heat sink assembly to join at least two thermal supports of the first heat sink assembly to attach the backside of the printed circuit board to dissipate the heat from the backside of the printed circuit board through the second heat sink assembly to at least two thermal supports of the first heat sink assembly.

¶0012 According to abovementioned objects, the present invention provides the heat sink device for the ball grid array package device with a modified embedded heat slug to improve the heat dissipating efficiency. The heat sink device is constructed of first heat sink assembly and the second heat sink assembly. The first heat sink assembly includes at least two thermal supports that located under the two sides of the first heat sink assembly, which is used to increase the heat dissipating area. A thermal block located below the backside of the heat sink, and the thermal block associated with the cavity of the ball grid array package device with modified embedded heat slug to increase the heat dissipating efficiency. In addition, the second heat sink assembly is a bottom plate, which includes a protruding structure in the center of the second heat sink assembly, and at least two openings on the two sides of the second heat sink assembly respectively. The protruding structure attached to the backside of the printed circuit board to increase the efficiency of heat dissipating and to remove the heat that is generated from the die or chip. In addition, at least two openings of the second heat sink assembly joined at least two thermal supports of the first heat sink assembly to fix the first heat sink assembly, the second heat sink assembly, and the ball grid array package device, in which the ball grid array package device positioned between the first heat sink assembly and second heat sink assembly.

BRIEF DESCRIPTION OF THE DRAWINGS

¶0013 The foregoing aspects and many of the attendant advantages of this invention will become more readily appreciated as the same becomes better understood by reference to the following detailed description, when taken in conjunction with the accompanying drawings, wherein:

¶0014 FIG. 1 is a schematic that shows the lateral view of device of the ball grid array package device with the modified embedded heat slug in accordance with the prior conventional technique;

¶0015 FIG. 2 is a schematic representation the cross-section view of device of the ball grid array package with modified embedded heat slug in accordance with the prior conventional technique;

¶0016 FIG. 3 is a schematic that shows the cross-sectional view of the structure of a first heat sink assembly that includes a first heat sink structure thereon, at least two thermal supports located under two sides of the heat sink body, and a thermal block that located on the backside of the first heat sink structure in accordance with the device disclosed herein;

¶0017 FIG.4 is a schematic that shows the cross-sectional view of the thermal conductive adhesive tape spread overall the surface of backside of the first heat sink structure to fix and to increase the heat dissipating efficiency in accordance with the device disclosed herein;

¶0018 FIG.5 is a schematic that shows the cross-sectional view of the structure of the ball grid array package device with modified embedded heat slug on the printed circuit board in accordance with the device disclosed herein;

¶0020 FIG. 6 is a schematic that shows the cross-sectional view of the structure of the second heat sink assembly in accordance with the device disclosed herein;

¶0021 FIG. 7 is a schematic that shows the cross-sectional view of the thermal support of the heat sink body passed through the at least two through holes of the printed circuit board to fix the printed circuit board in accordance with the device disclosed herein; and

¶0022 FIG. 8 is a schematic that shows the heat dissipating is introduced from the printed circuit board to the heat sink assembly in accordance with the structure disclosed herein.

¶0023 Some sample embodiments of the invention will now be described in greater detail. Nevertheless, it should be recognized that the present invention can be practiced in a wide range of other embodiments besides those explicitly described, and the scope of the present invention is expressly not limited except as specified in the accompanying claims.

¶0024 The present invention provides the package device with modified embedded heat slug to reduce the thermal resistance and to increase the heat dissipating capability. The package device can be a ball grid array package device. FIG. 3 through FIG. 7 illustrates the structure, function, and the relationship there-between of the heat sink device, and FIG. 8 represents the heat-dissipating path of the heat sink device of the present invention. FIG. 3 represents the structure of the first heat sink assembly 1A, which includes a first heat dissipating structure 2 and a second heat dissipating structure 4. The second heat dissipating structure 4, such as a heat dissipating fin, is disposed on the first heat dissipating structure

2. The second heat dissipating structure 4 is used to increase the heat dissipating area to improve the heat dissipating efficiency. At least two thermal supports 6 are located on the backside of the first heat dissipating structure 2, and are insertable through holes 14 in a printed circuit board 12 (as shown in FIG. 8). The first heat dissipating structure 2 is attached to the package device with a modified embedded heat slug on the printed circuit board (as shown in FIG. 5), and is connected with the second the heat sink assembly (as shown in FIG. 4).

¶0025 The key feature of the present invention is that the first heat dissipating structure 2 is made of conductive material such as metal, and is made by casting, therefore, the heat dissipating efficiency would be improved. Another key feature of the present invention is that at least two thermal supports 6 located on the backside of the first heat dissipating structure 2 are used instead of the conventional plastic supports to increase the heat dissipating efficiency.

¶0026 Referring to FIG. 3, the first heat dissipating structure 2 further includes a thermal block 8 on the backside of first heat dissipating structure 2. The thermal block 8 contacts with the walls defining cavity 24 of the package device with modified embedded heat slug to increase the heat dissipating efficiency. When a large quantity of heat is generated during the operation of the integrated circuit, the thermal block 8 helps to dissipate the heat. In an alternative preferred embodiment, the thermal conductive adhesive tape 10 is spread overall the surface of backside of the first heat dissipating structure 2 (as shown in FIG. 4). The thermal conductive adhesive tape 10 is connected with the surface of the molding compound 26 of the package device to increase the heat dissipation efficiency.

¶0027 FIG. 4 shows another alternative embodiment of the present invention. The thermal conductive adhesive tape 10 is located on the backside of the first heat dissipating structure 2 in which the thermal conductive adhesive tape 10 is spread around the backside

of the first heat dissipating structure 2, except the thermal block 8. The thermal block 8 on the backside of the first heat dissipating structure 2 can be an inseparable member with the first heat dissipating structure 2, or added on the backside of the first heat dissipating structure 2.

¶0028 FIG. 5 illustrates the structure of the package device with modified embedded heat slug on the printed circuit board. The structure includes a ball grid array package substrate 20 and a modified embedded heat slug 22. The modified embedded heat slug 22 is located on the ball grid array package substrate 20, and the modified embedded heat slug 22 has a cavity 24 therein in order to reduce the thickness of the molding compound 26. In addition, the plurality of balls 28 are located under the ball grid array package substrate 20 to connect with the printed circuit board 12. Furthermore, the printed circuit board 12 includes at least two holes 14 therein to permit at least two thermal supports 6 to pass through.

¶0029 In another embodiment, the first heat dissipating structure 2 does not have any thermal support 6, and the printed circuit board 12 does not have any holes 14 therein. Only the thermal block 8 assembles with the cavity 24 of the heat slug. Furthermore, a conductive material can be used as an adhesive material to adhere the first heat dissipating structure 2 and the ball grid array package device 22.

¶0030 FIG. 6 represents illustrates the structure of the second heat sink assembly 1B. The second heat sink assembly 1B is a bottom plate, which has at least two openings 34 on the two sides and a protruding structure 32 of the center of the bottom plate 1B. At least two openings 34 joined with at least two thermal supports 6 in order to fix the first heat sink assembly 1A and the second heat sink assembly 1B. And the openings 34 joined with the thermal supports 6 by the groove 7 on the side of the at least two thermal supports 6 and the blot 36 of the two openings 34.

¶0031 Furthermore, the protruding structure 32 can attach to the backside of the printed circuit board 12, therefore, the heat can be transferred from the bottom plate 1B through at least two thermal supports 6, and the second heat dissipating structure 4 to the outside. The advantage of the abovementioned description is that the heat dissipating space of the backside of the printed circuit board 12 can be increased to improve the heat dissipating efficiency. Moreover, the second heat sink structure 4 can use for the conventional BGA (ball grid array) package device or TEBGA (thermal enhanced ball grid array) package device without using thermal block 8.

¶0032 FIG. 7 shows how the first heat dissipating structure 2 is fixed with the printed circuit board 12. The thermal supports 6 of the first heat dissipating structure 2 pass through the holes 14 of the printed circuit board 12 to fix the backside of printed circuit board 12 with first heat dissipating structure 2. In view of the mechanical design, at least two the thermal supports 6 do not contact with the hole-wall of the holes 14, because there is a tolerance between at least two thermal supports 6 and the hole-wall of the hole 14. Nevertheless, the objective of the present invention is to improve the heat dissipating efficiency, thus the tolerance will reduce the heat dissipating efficiency. Therefore, after the thermal supports 6 are passed through the hole 14 of the grounded plane 12A, the gap space between the hole-wall of holes 14 and the thermal supports 6 are raised .

¶0033 Therefore, the heat can be removed from the grounded plane 12A of the printed circuit board 12 to the thermal supports 6 by the conductive material 42. And then, the heat is transferred form the thermal supports 6 and through the first heat dissipating structure 2 to the environment. On the other hand, the heat also can be removed from the ball grid array package device through the heat slug 22, the second heat sink assembly 1B, at least two thermal supports 6, and the grounded plane 12A of the printed circuit board 12 to the environment. Moreover, the heat sink assembly also can be used only with at least two thermal supports 6, and at least two springs, but without the bottom plate 1B.

¶0034 The second heat sink assembly 1B is made of a conductive material or metal, and the second heat sink assembly 1B has the protruding structure 32 attached to the heat slug 22. Therefore, the heat can be transferred to the environment by the second heat sink assembly 1B to improve the heat dissipating efficiency of the package device. Furthermore, the second heat sink assembly 1B further includes at least two springs 40 that put around at least two thermal supports 6 to pull the first heat sink assembly 1A and the second heat sink assembly 1B tight for the heat dissipating between them.

¶0035 FIG. 8 is cross-sectional view of the construction of the package device with heat sink device 1. The heat would be generated from the chip or die during the computer is operated, thus the heat should be removed to reduce the operating temperature to keep the computer operating stability. The heat can be removed by one heat dissipating path. The heat dissipating path is formed by the heat to the heat slug 22, the thermal block 8, first heat dissipating structure 2, and second heat dissipating structure 4. The heat is removed from the chip and transferred through the heat slug 22 of package device to the thermal block 8, first heat dissipating structure 2, and second heat dissipating structure 4 to the environment. There is another heat dissipating path of the package of the present invention. This heat dissipating path is formed by the backside of the printed circuit board 12, the thermal supports 6, the first heat sink assembly 1A and the second heat sink assembly 1B. The heat is removed from the chip and transferred through the backside of the printed circuit board 12, the thermal supports 6, the first heat sink assembly 1A, or transferred through down through at least two springs 40, at least two openings 34 of two sides of the second heat sink assembly 1B, and the printed circuit board grounded layer 12A to the environment. There is still another heat dissipating path of the package of the present invention. This heat dissipating path is formed by the backside of the printed circuit board 12 and the protruding structure 32 of the second heat sink assembly 1B. The heat also can be removed from the chip and transferred through the backside of the printed circuit board 12, the protruding

structure 32 of the second heat sink assembly 1B and the printed circuit board grounded plane 12A to the environment.

¶0036 Although specific embodiments have been illustrated and described, it will be obvious to those skilled in the art that various modifications may be made without departing from what is intended to be limited solely by the appended claims.